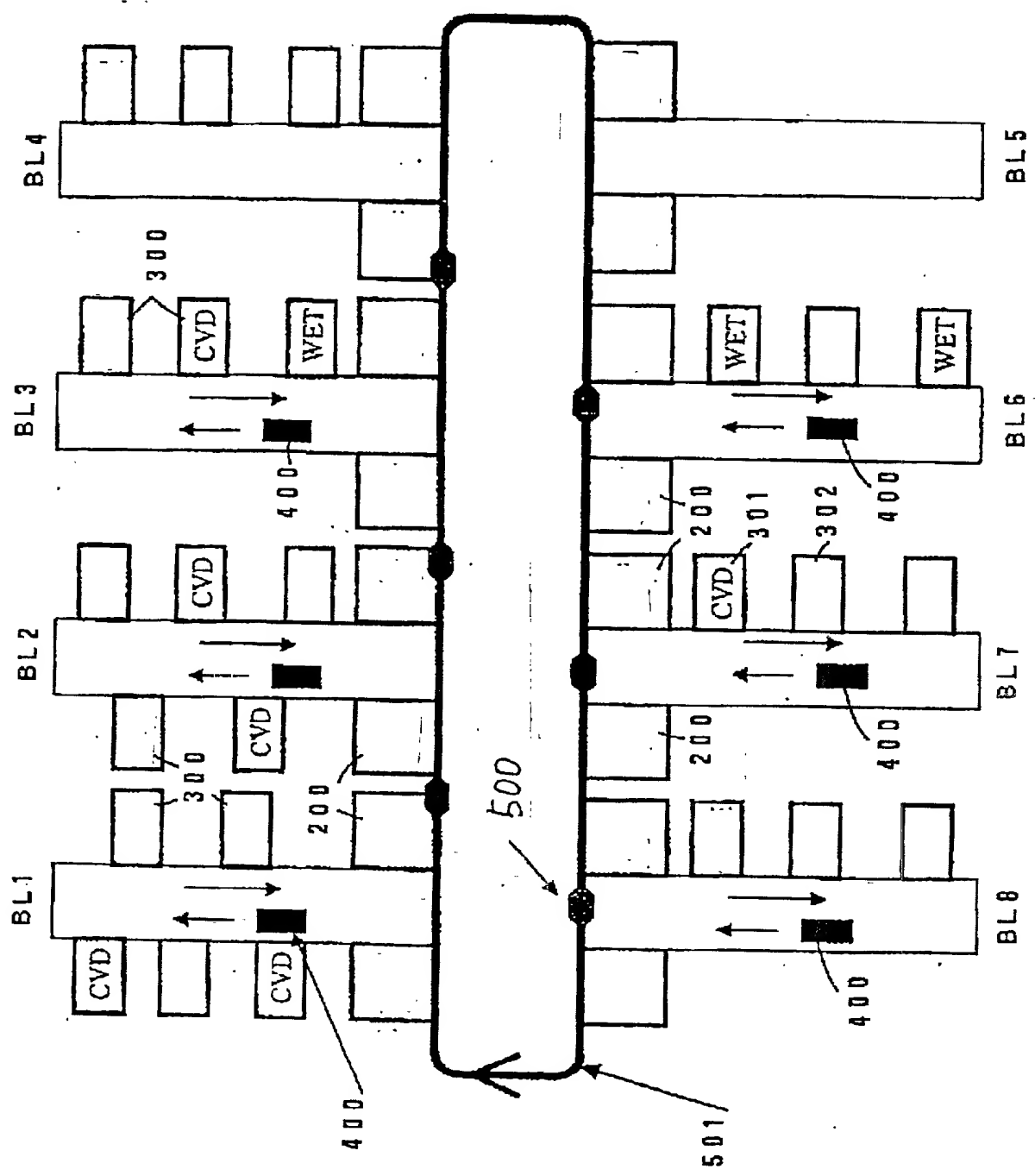


FIG. 1 prior art

FIG. 1 prior art



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FIG. 2 prior art

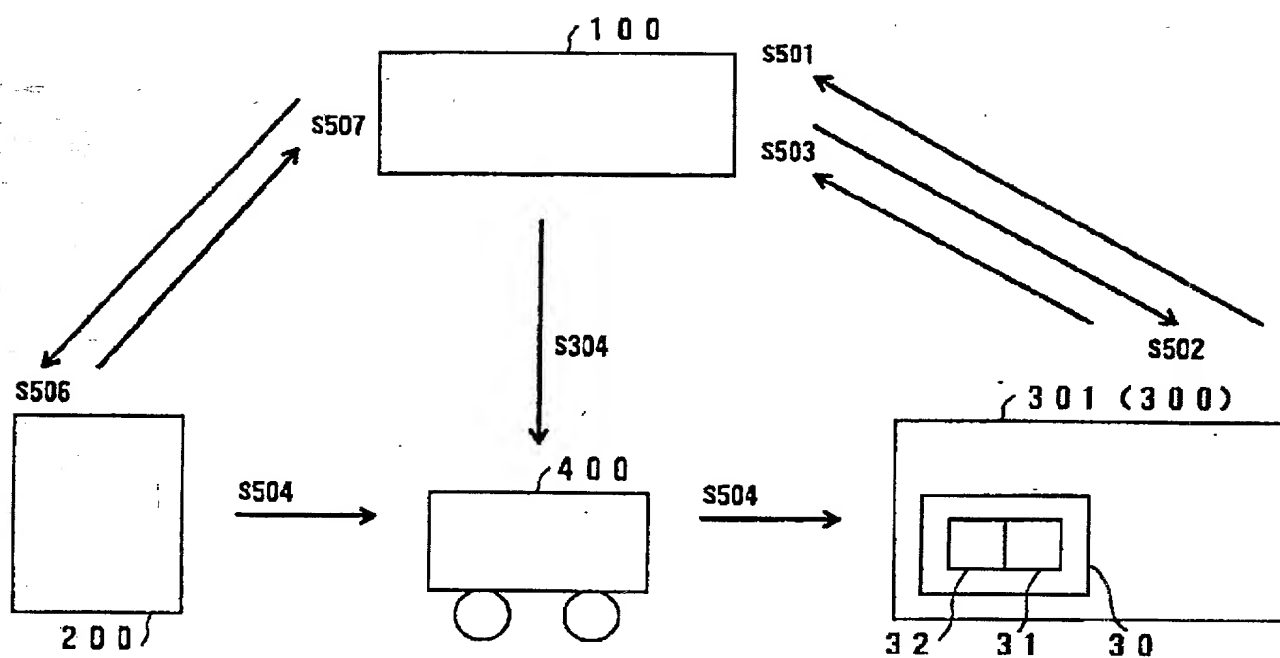


FIG. 3

low pressure CVD system

host computer

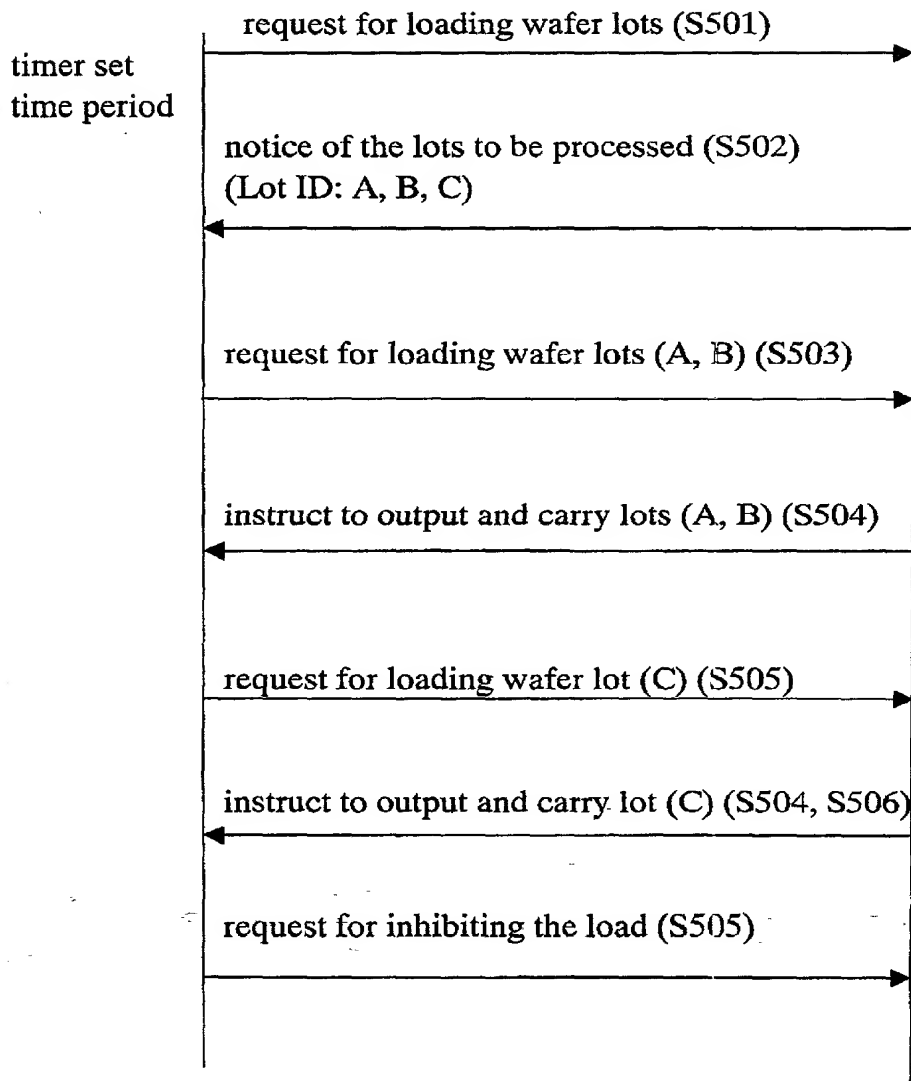
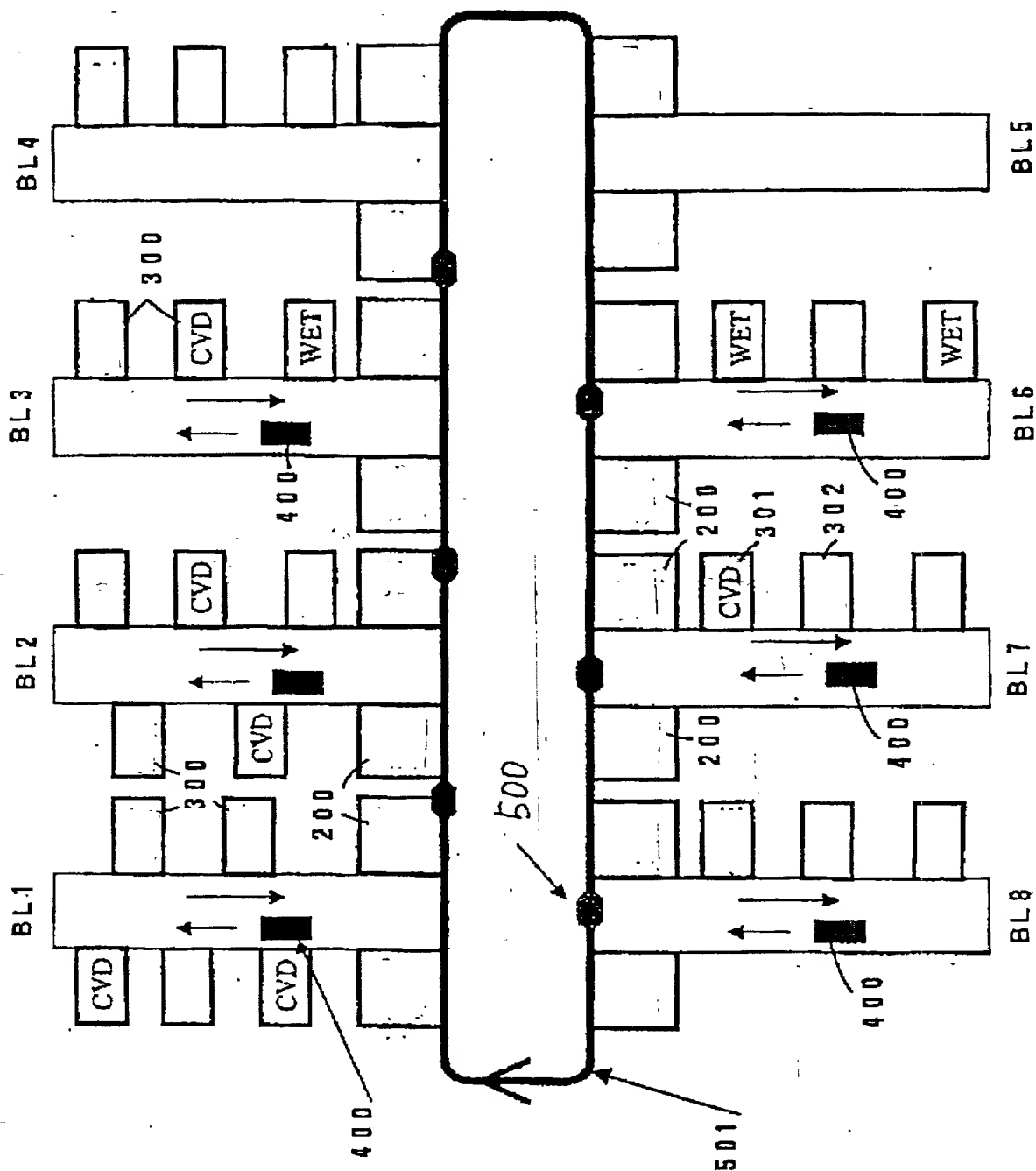
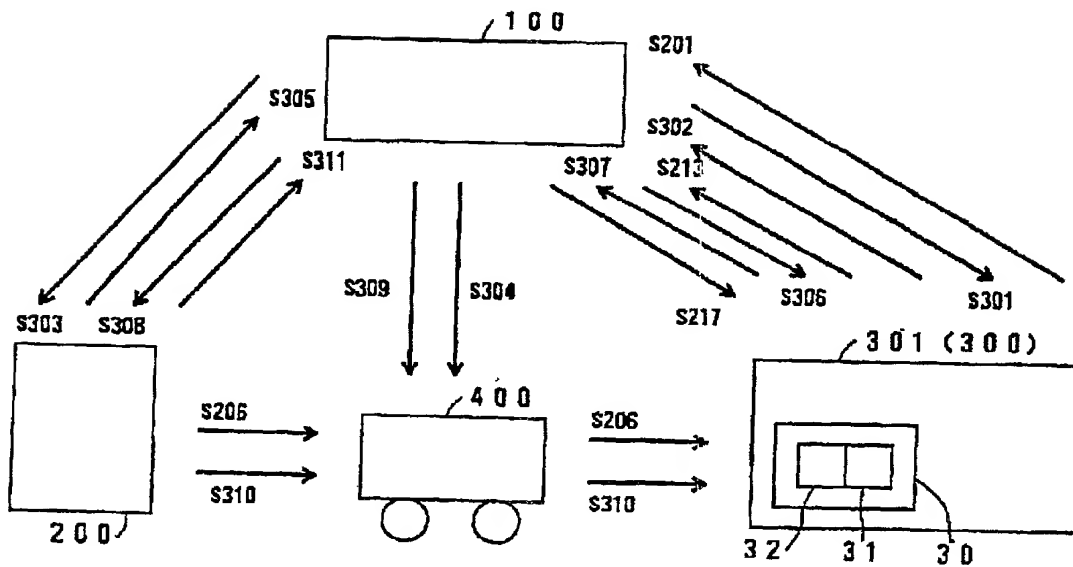


FIG. 4



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FIG. 5



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FIG. 6

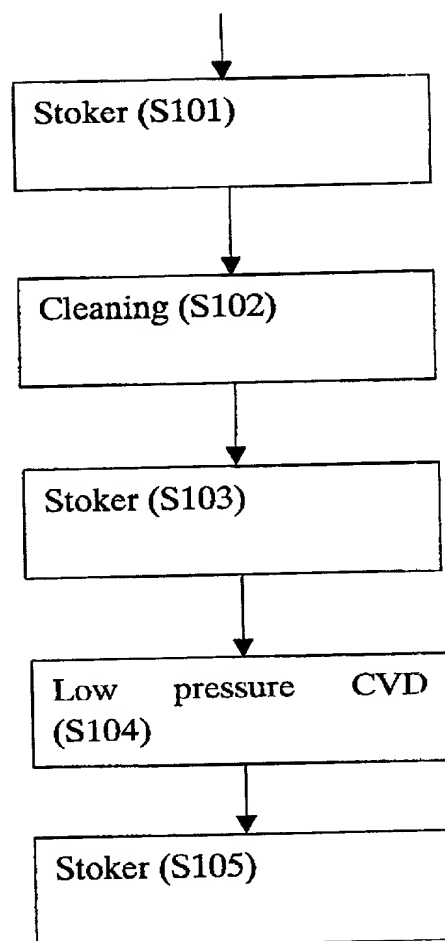


FIG. 7

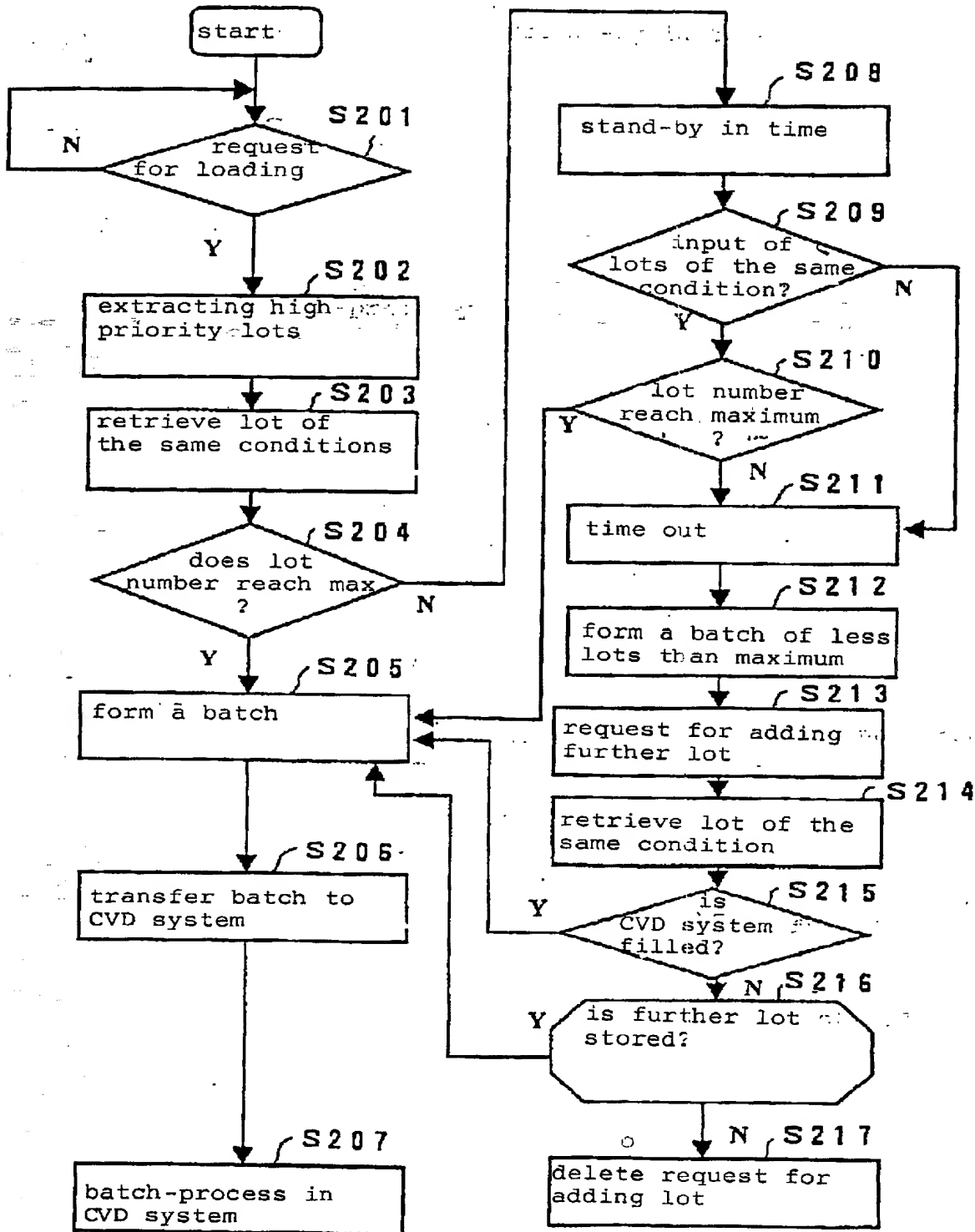


FIG. 8

low pressure CVD system

host computer

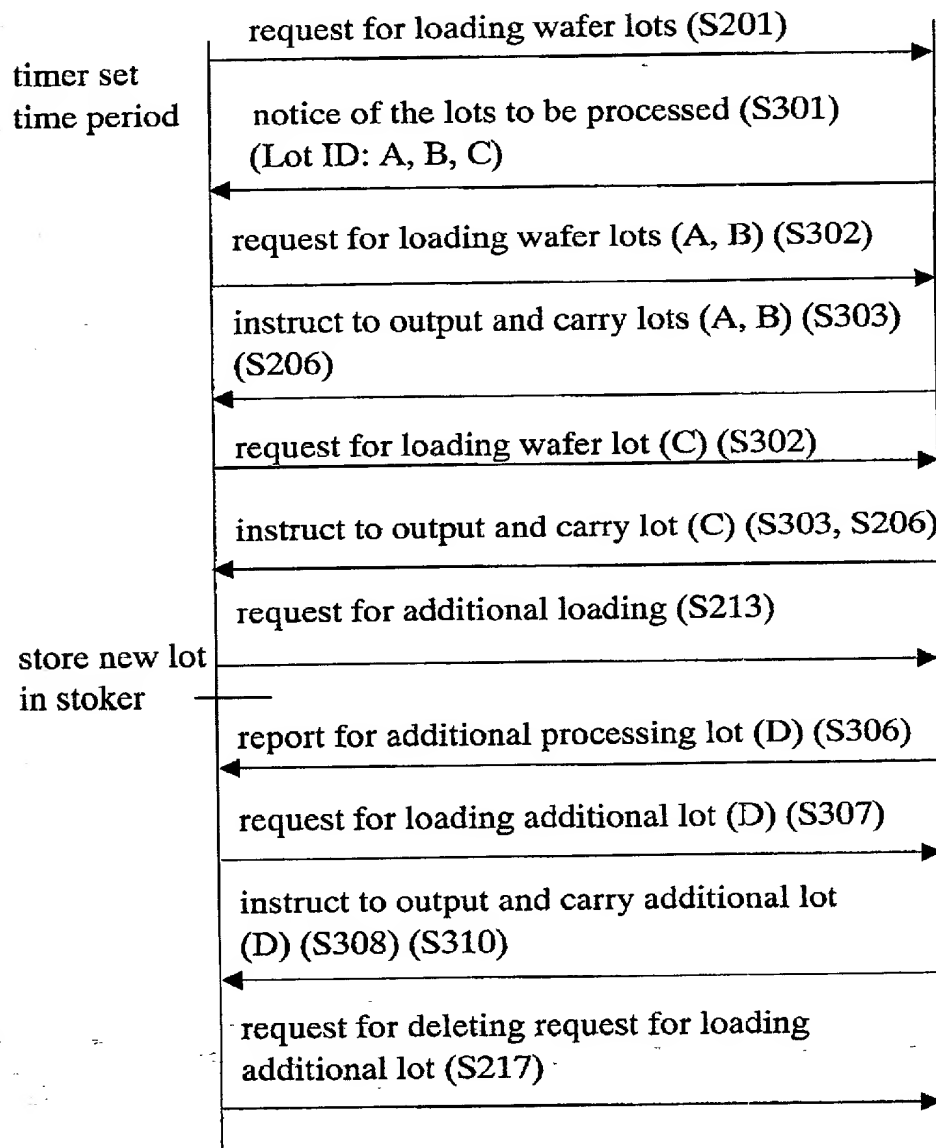




FIG. 9

